

ABSTRACT

A p-type well is formed in an upper layer of a silicon substrate. Arsenic ions are implanted into an extreme surface layer of the p-type
5 well and a heat treatment is performed to form a p-type low-concentration layer. A HfAlO_x film and a polycrystalline silicon layer are laminated on the substrate. A gate electrode is formed by patterning the polycrystalline silicon layer. After a n-type extension region is formed by implanting arsenic ions by using the
10 gate electrode as a mask, sidewall spacers are formed on sides of the gate electrode. Arsenic ions are implanted by using the sidewall spacers and the gate electrode as masks to form n-type source/drain regions.